# **Progress Towards III-V Photovoltaics on Flexible Substrates**

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Presented here is the recent progress of the NASA Glenn Research Center OMVPE group's efforts in the development of high efficiency thin-film polycrystalline III-V photovoltaics on optimum substrates. By using bulk polycrystalline germanium (Ge) films, devices of high efficiency and low mass will be developed and incorporated onto low-cost flexible substrates. Our progress towards the integration of high efficiency polycrystalline III-V devices and recrystallized Ge films on thin metal foils is discussed.

## **Nomenclature**

AM0 = Air Mass Zero

*I-V* = Current versus Voltage

*OMVPE* = Organo-Metallic Vapor Phase Epitaxy

PV = photovoltaics

## I. Introduction

NASA and other government agencies have invested heavily in silicon and II-VI based thin film photovoltaics (PV) technologies with the hopes of delivering both high efficiency (~15%) and low mass for both space and terrestrial applications. Despite significant investment, device efficiencies have failed to meet target goals and have demonstrated limited progress in recent years. This has led most agencies to divest themselves of thin film PV research programs. The traditional approaches have tried to make something that is inherently lightweight, highly efficient. Our approach is to take something with demonstrated high efficiency and make it lightweight and flexible.

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For near Earth applications, absolute conversion efficiency has become the dominant metric driving solar cell development. As missions move beyond low Earth orbit, specific mass and stowage volume (W/kg and W/m³) become ever more important. Fig. 1 shows a comparison of cell mass specific powers for III-V multijunction solar

cells deposited on various substrates. Current technology is shown by the 29% AM0 efficiency on 150 micron Ge substrates (~ 450 W/kg). If silicon is substituted for Ge, the specific power nearly doubles despite a predicted drop in overall efficiency. Transitioning to a metal foil provides the potential for an additional increase in specific power, plus the added feature of flexibility. The impact of the high density of molybdenum can be moderated by the possible addition of a polymer (Kapton) backing layer following device fabrications. Finally, if a high temperature polymer substrate developed, very high mass specific power may be achievable.

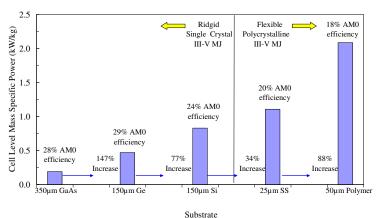


Figure 1. Cell level mass specific power for various thin film substrates.

To reach the high mass specific power metric, we are integrating two demonstrated technologies, (1) recrystallized germanium (Ge) films on thin metal foils and (2) high efficiency polycrystalline III-V photovoltaics, into a high specific power (> 1000 W/kg), high efficiency (>20% AM0), flexible solar array technology. Several research groups have demonstrated the unique ability of thin Ge films deposited on metal or ceramic substrates to form large crystal grains (> 1mm²) under the appropriate thermal annealing conditions¹-². In addition, we have demonstrated high efficiency III-V photovoltaics (gallium arsenide - GaAs) on bulk polycrystalline Ge wafers with ~1 mm² average grain sizes. The successful integration of these two technologies appears eminently feasible and highly desirable. The first steps down this path have been taken with the successful demonstration of III-V single and multi-junction devices on Si substrates⁻-³-⁵. Moving to the next step, polycrystalline III-V devices on Mo foil, builds upon component technologies demonstrated previously.

In order to reach the efficiencies projected (20%), we are developing an indium gallium phosphide (InGaP) device to integrate on top of our existing GaAs device, thus forming a high efficiency multijunction or tandem solar cell. InGaP/GaAs tandem devices have become the industry standard for space photovoltaics. Our previous polycrystalline GaAs devices included InGaP passivating window layers, thus we have confidence that this material can be grown in a polycrystalline format and that a high efficiency device can be developed. At the completion of the effort, a high efficiency (20% AM0) polycrystalline InGaP/GaAs tandem will be fabricated on the recrystallized Ge film on Mo foil.

## II. Experimental

Significant progress has been made on the development of recrystallized germanium thin films on molybdenum

substrates. Early work included a systematic variation in recrystallization processes (layer structure, layer thicknesses, annealing temperatures and annealing environments), which have better focused our efforts. Our current method follows the structure seen in Fig. 2 where a polished Mo disk has been sputter coated with an ultra-thin W-Ti layer and a thin Ge surface layer. The sample is then annealed at 675 °C in a H<sub>2</sub>/AsH<sub>3</sub> environment for 15 minutes. During annealing, As diffuses into the Ge layer converting it from p-type conductivity (as deposited) to n-type (as desired). In addition, a

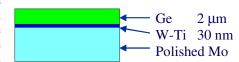


Figure 2. Schematic showing the sample structure and approximate thicknesses of the W-Ti and Ge films.

triggered nucleation of grain growth on the Ge surface is seen. Scanning electron microscopy (SEM) and optical Nomarski microscopy show the diameter of the grains to be greater than 100 µm (Fig. 3).

The OMVPE growth of the GaAs based devices followed the conventional practices used in the space photovoltaics industry and can be found elsewhere<sup>6</sup>. The basic cell structure which was used in this work is shown in Fig. 4. Full solar cell devices were grown on both commercial polycrystalline Ge wafers and the previously mentioned recrystallized Ge on metal foils. The samples were fabricated into working devices following

conventional photolithography and metallization techniques. These samples were then characterized by standard I-V measurements under a simulated 1 sun AM0 spectrum and by quantum efficiency measurements.

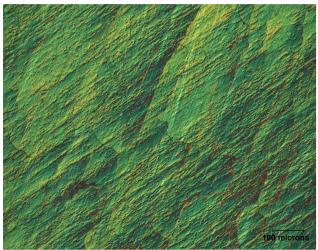


Figure 3. Nomarski microscopy image of the surface of a recrystallized Ge film. The grains are greater than 100 um in diameter.

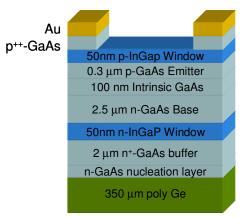


Figure 4. Growth stack schematic for polycrystalline GaAs solar cell

## III. Results

The efforts to replicate the Ge crystallographic morphology of the commercially available bulk polycrystalline wafers on a thin metal foil are seen in Fig. 5. A nexus of the grain growth is seen at the bottom of the photograph. The annealing did not produce a large degree of grain growth, and the resulting grains were on the order of  $10~\mu m^2$  or less. However, the crystallinity of the as-deposited Ge did improve as shown in Fig. 6.

The I-V results of the cell grown on a commercially obtained 350  $\mu$ m thick polycrystalline Ge wafer is shown in Fig. 7. This cell has a very respectable open circuit voltage ( $V_{oc}$ ) of ~ 1.0 V and a short-circuit current density ( $I_{sc}$ ), without anti-reflective coating of 23 mA/cm<sub>2</sub>, under a simulated 1 sun AM0 spectrum. Accounting for reflectance losses, it is estimated that this cell would have approximately a 17 % AM0 conversion efficiency.



Figure 5. A digital photograph of the Ge/W-Ti/Mo foil, where a nexus of grain growth is observed in near the edge at the bottom of the image.

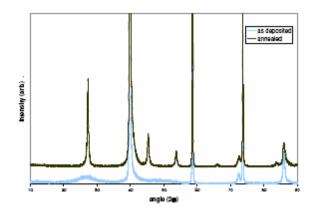
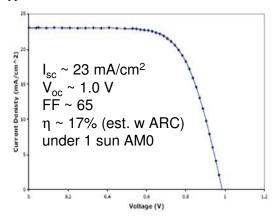


Figure 6. X-ray diffraction results for as-deposited and Ge films anneals at  $675\,^{\circ}\text{C}$  in AsH<sub>3</sub>.

The current versus voltage results under a simulated 1 sun AM0 spectrum for the recrystallized Ge cell are given in Fig. 8. Unfortunately there was a substantial decrease in the  $I_{sc}$ ,  $V_{oc}$ , and the fill factor over the control

polycrystalline device. Although the actual device performance was limited, this demonstration a thin film lightweight and flexible III-V device on a low-cost metallic substrate did achieve a specific power comparable to the best space PV devices currently available. Spectral response data showed no surprises and basically had a uniformly reduced intensity over the approximately 370 to 910 nm range of interest. These initial results were undoubtedly limited by the small grain sizes and rough morphology achieved by this approach. The use of capping layers to promote grain growth and better surface morphology should prove beneficial to improving the performance of this type of a device.



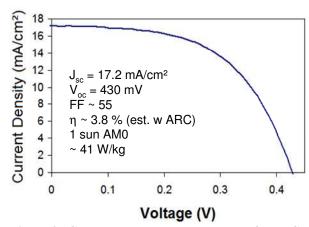


Figure 7. I-V curve for a GaAs cell deposited on a 350 um commercial polycrystalline Ge substrate.

Figure 8. Current versus voltage results for a GaAs p/i/n solar cell grown on a Ge/Ti-W/Mo foil substrate.

Optimization of GaAs cell performance is continuing with the optimization of base doping levels, base dopant species and overall device structure. We are currently in the process of developing a hydrogen sulfide (H<sub>2</sub>S) dopant source. It is hoped that sulfur will diffuse to the grain boundaries and create a passivating electric field which will improve the cell performance. We have also recently produced a heterojunction GaAs wherein the GaAs emitter was replaced with a InGaP emitter. This device demonstrated an enhanced Voc (80mV increase) despite having a lower current due to absorption losses in the InGaP emitter. These losses will not negatively impact multijunction devices and thus may be incorporated in future multijunctions.

## **IV.** Conclusion

A GaAs-based p-i-n solar cell which had a  $V_{oc}$  of approximately 1.0 V and  $I_{sc}$  of nearly 23 mA/cm² (device without antireflection (AR) coating) under 1 sun AM0 illumination was produced on a commercial polycrystalline Ge wafer. This result translates to an estimated AM0 efficiency of nearly 17% using a suitable AR coating. This same device structure was used to produce a solar cell on a re-crystallized Ge thin film on a Mo foil substrate, and yielded a specific power of over 40 W/kg when measured under a simulated 1 sun AM0 spectrum. This result, albeit somewhat modest, is nonetheless encouraging when considering the small grain size of the polycrystalline Ge film, and obvious problems seen in surface morphology of the foils used to date.

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